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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Complete if Known

Application Number	09/651,425
Filing Date	August 30, 2000
First Named Inventor	Christopher Songer
Group Art Unit	Not Assigned
Examiner Name	Not Assigned
Attorney Docket Number	03048.P008

Sheet 1 of 2

U.S. PATENT DOCUMENTS

Examiner Initials *	U.S. Patent Document Number	Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Filing Date if Appropriate
WJ	6,122,719	Mirsky		
WJ	5,915,123	Mirsky		
WJ	6,108,760	Mirsky		
WJ	5,742,180	DeHon et al.		
WJ	4,967,340	Dawes		
WJ	5,956,518	DeHon et al.		

FOREIGN PATENT DOCUMENTS

Examiner Initials *	Foreign Patent Document	Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Translation? Yes/No
	Office or Country Number Date			

OTHER DOCUMENTS

Examiner Initials *	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published (if known).	Translation? Yes/No
NZ	"Smart Compilers Puncture Code Bloat," Brown, Electronic Engineering Times, October 9, 1995 (pages 38 & 42).	
NZ	"A High-Performance Microarchitecture with Hardware-Programmable Functional Units," Razdan et al., Micro-27 Proceedings of the 27th Annual International Symposium on Microarchitecture, 11/30-12/2/94 (pp. 172-180).	
NC	"Programmable Active Memories: Reconfigurable Systems Come of Age," IEEE Transactions on VLSI Systems, 1995 (pp. 1-15).	
NZ	"Pilkington Preps Reconfigurable Video DSP," Clark, EE Times, week of July 31, 1995.	
NC	"Coarse-Grain Reconfigurable Computing," Mirsky, Ethan A., Thesis submitted at the Massachusetts Institute of Technology, June 1996.	

Examiner Signature

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Date Considered

1-15-05

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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Sheet	2	of	2
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PC	"SOP: Adaptive Massively Parallel System," by Tsukasa Yamauchi et al., NEC Research & Development, Vol. 37, No. 3, July 1996 (pp. 382-393).	

1-15-05

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